IN THE SPECIFICATION

Please replace paragraph 0038.1 with the following:

[0038.1] FIGURE 5 is a block diagram describing circuitry 550. The circuitry comprises multiplexers 555(0)…555(31) for providing selectively providing data word 611 via ~~p9ort~~ port 560 in either forward or reverse order based on a signal REVERSE.

Please replace paragraph 0039 with the following:

[0039] Referring now to **FIGURE 6** ~~**FIGURE 5**~~, there is illustrated a flow diagram for providing a video packet in a reverse order. At 705, the state logic machine 605 receives a command to fetch data words in an address range, e.g., 505(x)-510(n) from the MPEG video decoder 445, accompanied by a control signal indicating that the data words in the address range are to be provided to the MPEG video decoder 445 in the reverse order, e.g., 505(n), 505(n-1)...505(x).